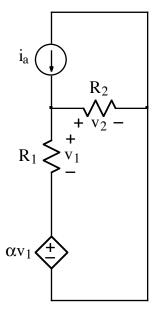
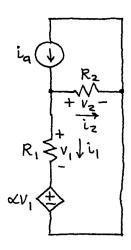
Ex:



- a) Derive an expression for v_2 . The expression must not contain more than the circuit parameters α , i_a , R_1 , and R_2 . **Note:** $\alpha > 0$.
- b) Make at least one consistency check (other than a units check) on your expression. Explain the consistency check clearly.

soln: a) Label resistor currents.



We have a v-loop on the bottom:

$$+ \alpha V_1 + V_1 - V_2 = OV$$

All other loops would pass thru a current prc.

We can write i-sum eghs for the node on the left or right but we only need one of them.

For the left node, our i-sum egh is

$$-iq + iz + i_1 = OA$$

We have no components in series carrying the same current, (other than a v-src and R).

Last, we write Ohm's law egins for R_1 and R_2 :

$$V_1 = \hat{\iota}_1 R_1$$

$$V_2 = i_2 R_2$$

substituting the Ohm's law expressions into the v-loop egh gives

$$\alpha i_1 R_1 + i_1 R_1 - i_2 R_2 = ov$$

or
$$i_1(x+1)R_1 - i_2R_2 = 0$$

Now we solve our i-sum egh for i_1 . (This is convenient since we will retain i_2 and be able to write an egh for v_2 .)

Substituting into our last v-loop egh, we have

$$(i_a - i_z)(\alpha + 1) R_1 - i_z R_z = OV$$

We simplify and solve for iz:

$$i_{\alpha}(\alpha+1)R_{1}=i_{2}[(\alpha+1)R_{1}+R_{2}]$$

or
$$i_2 = i_q \frac{(\alpha+1)R_1}{(\alpha+1)R_1 + R_2}$$

b) Many consistency checks are possible.

ex: Set a=0 so dependent v-src becomes a wire. Circuit becomes i-divider.

$$i_2 = i_q \frac{R_1}{R_1 + R_2}$$

Plug x=0 into answer from (a):

$$i_2 = i_q \frac{(0+1)R_1}{(0+1)R_1 + R_2} = i_q \frac{R_1}{R_1 + R_2}$$

(We get the same answer.)

ex: Set $R_2 = 0.52$ (a wire). All current will flow thru the wire since it is the path of least resistance.

Plug Rz = 0.52 into answer from (a):

$$i_2 = i_q \frac{(x+1)R_1}{(x+1)R_1 + 0.52} = i_q \sqrt{\text{(same answer)}}$$