

To pass the unit exam, you must be able to do the following (using books and notes):

| <u>CONCEPTUAL TOOLS</u> | Learning Objective | Reading |
|---|---|--------------------------------|
| <p>RLC CIRCUITS</p> <p>C (CAPACITOR) EQUATIONS</p> <p>$i = C dv/dt$ Series capacitors Parallel capacitors Initial conditions C = OPEN CIRCUIT CHARGE SHARING V SRC MODEL Final conditions open circuit Energy stored Example 1 (pdf) Example 2 (pdf)</p> <p>L (INDUCTOR) EQUATIONS</p> <p>$v = L di/dt$ Series inductors Parallel inductors Initial conditions L = WIRE CURRENT DIVISION I SRC MODEL Final conditions wire Energy stored Example 1 (pdf) Example 2 (pdf)</p> | <p>3.1 For a specified current through an inductance, find the voltage across it, and vice versa. For a specified voltage across a capacitance, find the current through it, and vice versa. From the voltages and currents, find energy stored in inductances and capacitances. Find the equivalence of inductances in series and parallel and of capacitances in series and parallel.</p> | <p>Chap 6: Sec 6.1-6.3</p> |
| <p>RLC CIRCUITS</p> <p>GENERAL RC/RL SOLUTION</p> <p>General solution Time const Thev equiv Solution procedure Example 1 (pdf) Example 2 (pdf)</p> | <p>3.2 Find the natural response of any circuit containing just one inductance or one capacitance (or one equivalent inductance or one equivalent capacitance).</p> | <p>Chap 7: Sec 7.1-7.2</p> |
| <p>RLC CIRCUITS</p> <p>GENERAL RC/RL SOLUTION</p> <p>General solution Time const Thev equiv Solution procedure Example 3 (pdf) Example 4 (pdf)</p> | <p>3.3 Find the step-function response of any circuit containing just one inductance or one capacitance (or one equivalent inductance or one equivalent capacitance).</p> | <p>Chap 7: Sec 7.3</p> |

* The material in this handout is based extensively on concepts developed by C. H. Durney, Professor Emeritus of the University of Utah.

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| <p>RLC CIRCUITS</p> <p>GENERAL RC/RL SOLUTION</p> <p>General solution Time const Thev equiv Solution procedure Example 5 (pdf) Example 6 (pdf) Example 7 (pdf)</p> | <p>3.4 For given RC and RL circuits (containing only one equivalent storage element) give qualitative explanations based on the interpretations that: (1) uncharged capacitance looks initially like a short circuit and finally like an open circuit, and (2) inductance with no initial current looks initially like an open circuit and finally like a short circuit.</p> | <p>Chap 7: Sec 7.4</p> |
| <p>CIRCUITS</p> <p>MAX POWER XFER</p> <p>Example (pdf)</p> | <p>3.5 Apply the maximum power transfer theorem.</p> | <p>Chap 4: Sec 4.12</p> |
| <p>SUPERPOSITION CIRCUITS</p> <p>$V_{DC} + V_{DC}$</p> <p>EXAMPLE 1 (PDF) EXAMPLE 2 (PDF)</p> | <p>3.6 Apply the principle of superposition.</p> | <p>Chap 4: Sec 4.13</p> |