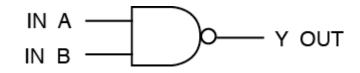
## Digital Circuits Neil E Cotter ECE DEPT. UNIVERSITY OF UTAH



# **Digital Electronics**

#### NAND



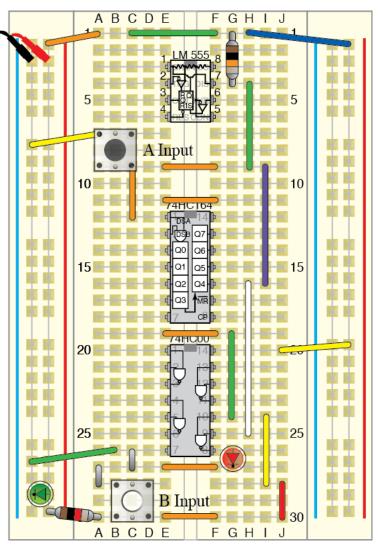
| IN A | IN B | OUT |
|------|------|-----|
| 0    | 0    | 1   |
| 0    | 1    | 1   |
| 1    | 0    | 1   |
| 1    | 1    | 0   |



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## **Digital Electronics: NAND Gate**

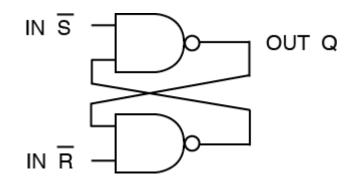
NAND Gate





## **Digital Electronics**

#### Flip-Flop



| R | S | OUT      |
|---|---|----------|
| 0 | 1 | 1        |
| 1 | 0 | 0        |
| 0 | 0 | Last Out |



## **Digital Electronics: Flip Flop**

Flip-Flop ABCDE FGHIJ M 55 Ľ. R. 5 **康 康** K R. Set ł 10 10 **成** (4) 74HC164 南南 Q0 K 15 15 Q1 0 K 02 0 03 CB 74HC00 20 201 R. R. **ĸ** ĸ 25 25 K k Ŕ Reset D 30 . ABCDE FGHIJ

